

**REMARKS**

Claims 1-61 are pending in the present application.

In the Office Action dated July 15, 2005 in the parent application, claims 1-5, 10-12, 49-54, and 56-59 were rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by Laczko, et al (U.S. Patent No. 6,775,778). The Examiner's rejections are respectfully traversed.

Laczko describes a central processing unit 131 that includes digital signal processing capabilities so that the central processing unit 131 can operate as a software cable modem and/or voice band modem. See Laczko, col. 4, ll. 30-37. However, Laczko is completely silent with regard to transmitting and/or receiving signals over a wireless communication channel, as set forth in independent claims 1 and 49. For at least this reason, Applicants respectfully submit that the present invention is not anticipated by Laczko and request that the Examiner's rejections of claims 1-5, 10-12, 49-54, and 56-59 under 35 U.S.C. 102(e) be withdrawn.

In the Office Action, claims 15-19, 23, 25, 26-30, 34, 36, 37-41, 46, and 48 were rejected under 35 U.S.C. § 102(a) as allegedly being anticipated by Bialick, et al (U.S. Patent No. 6,088,802). The Examiner's rejections are respectfully traversed.

Bialick is concerned with enabling a peripheral device to perform security operations, such as encryption of data. Bialick describes a peripheral device driver that enables the use of the peripheral device. The Examiner alleges that the peripheral device driver described by Bialick is a secure device driver that may be loaded by a processor complex. Applicants respectfully disagree. Bialick teaches that if the host computing device supports "plug and play," then the host computing device automatically provides the user with the opportunity to instruct the host computing device to transfer the peripheral device driver from the peripheral device to the host computing device. See Bialick, col. 9, ll. 5-25. However, Bialick does not teach or

suggest applying any type of security mechanism to the peripheral device drivers before, during, or after transferring the peripheral device driver from the peripheral device to the host computing device. Accordingly, Applicants respectfully submit that the peripheral device driver described by Bialick is not a secure device driver, as defined in the specification and set forth in independent claims 15, 26, and 37.

For at least this reason, Applicants respectfully submit that the present invention is not anticipated by Bialick and request that the Examiner's rejections of claims 15-19, 23, 25, 26-30, 34, 36, 37-41, 46, and 48 under 35 U.S.C. 102(a) be withdrawn.

In the Office Action, claims 6-9, 14, 55, and 61 were rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Laczko in view of Bialick. The Examiner's rejections are respectfully traversed.

Claims 6-9 and 14 depend from independent claim 1, and claims 55 and 61 depend from independent claim 49. To establish a *prima facie* case of obviousness, the prior art reference (or references when combined) must teach or suggest all the claim limitations. As discussed above, Laczko is completely silent with regard to transmitting and/or receiving signals over a wireless communication channel, as set forth in independent claims 1 and 49. Bialick is also completely silent with regard to transmitting and/or receiving signals over a wireless communication channel, as set forth in independent claims 1 and 49. For at least this reason, Applicants respectfully submit that the present invention is not obvious over the prior art of record and request that the Examiner's rejections of claims 6-9, 14, 55, and 61 under 35 U.S.C. 103(a) be withdrawn.

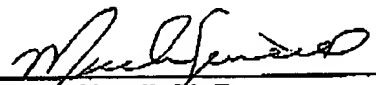
It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc.'s Deposit No. 01-0365/TT4040.

The Examiner is invited to contact the undersigned at (713) 934-4052 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

Date: \_\_\_\_\_

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